IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Koichi NANIWAE Conf. 9283

Application No. 10/573,492 Group 2892

Filed May 15, 2006 Examiner E. Jones

METHOD OF CLEANING TREATMENT AND METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

RESPONSE AFTER FINAL REJECTION

Assistant Commissioner for Patents May 19, 2010 P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

This responds to the Official Action of February 19, 2010.

Remarks begin on page 2 of this paper.